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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/667,046	09/21/2000	Steven A. Lytle	LYTLE 18	8375	
27964 7	590 04/01/2003				
HITT GAINES & BOISBRUN P.C.			EXAMINER		
P.O. BOX 832 RICHARDSO	BOX 832570 HARDSON, TX 75083		VU, HUNG K		
			ART UNIT	PAPER NUMBER	
			2811		
1			DATE MAILED: 04/01/2003	DATE MAILED: 04/01/2003	

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>4</b>			Un
	Application No.	Applicant(s)	<u> </u>
Advisory Action	09/667,046	LYTLE, STEVEN A.	
,	Examiner	Art Unit	
	Hung K. Vu	2811	
The MAILING DATE of this communication appe	ars on the cover sheet with the c	orrespondence addr	ess
THE REPLY FILED 24 March 2003 FAILS TO PLACE T Therefore, further action by the applicant is required to a final rejection under 37 CFR 1.113 may only be either: (1 condition for allowance; (2) a timely filed Notice of Appea Examination (RCE) in compliance with 37 CFR 1.114.	void abandonment of this applice ) a timely filed amendment whi	cation. A proper rep ich places the applic	ly to a ation in
PERIOD FOR RE	PLY [check either a) or b)]		
a) The period for reply expiresmonths from the mailing of			
b) The period for reply expires on: (1) the mailing date of this Adv event, however, will the statutory period for reply expire later the ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS 706.07(f). Extensions of time may be obtained under 37 CFR 1.136(a). The dat have been filed is the date for purposes of determining the period of extens 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened (b) above, if checked. Any reply received by the Office later than three mo earned patent term adjustment. See 37 CFR 1.704(b).	an SIX MONTHS from the mailing date of FILED WITHIN TWO MONTHS OF THE e on which the petition under 37 CFR 1.1 sion and the corresponding amount of the statutory period for reply originally set in	f the final rejection. E FINAL REJECTION. So 136(a) and the appropriate e fee. The appropriate exte the final Office action; or (	ee MPEP extension fee ension fee under 2) as set forth in
1. A Notice of Appeal was filed on Appellant's 37 CFR 1.192(a), or any extension thereof (37 CFI	•		
2. $\square$ The proposed amendment(s) will not be entered be	ecause:		
(a)   they raise new issues that would require further	er consideration and/or search (	see NOTE below);	
(b) they raise the issue of new matter (see Note b	pelow);		
(c) they are not deemed to place the application i issues for appeal; and/or	n better form for appeal by mat	erially reducing or si	implifying the
(d) they present additional claims without cancel NOTE:	ing a corresponding number of	finally rejected claim	ıs.
3. Applicant's reply has overcome the following reject	ion(s):		
4. Newly proposed or amended claim(s) would canceling the non-allowable claim(s).	be allowable if submitted in a s	eparate, timely filed	amendment
5. ☑ The a) ☐ affidavit, b) ☐ exhibit, or c) ☑ request for application in condition for allowance because: See		sidered but does NO	T place the
6. The affidavit or exhibit will NOT be considered bed raised by the Examiner in the final rejection.	cause it is not directed SOLELY	to issues which wer	e newly
7. For purposes of Appeal, the proposed amendment explanation of how the new or amended claims we			and an
The status of the claim(s) is (or will be) as follows:			
Claim(s) allowed:			
Claim(s) objected to:			
Claim(s) rejected: <u>21,24,25,29,30</u> .			
Claim(s) withdrawn from consideration:			
8. The proposed drawing correction filed on is	a) approved or b) disapp	proved by the Exam	iner.
9. Note the attached Information Disclosure Statemen	nt(s)( PTO-14 <u>49)</u> Paper No(s).	<del></del> '	
10. Other:	TOM THOMAS	(アゥル・S) ー	
	SUPERVISORY PATENT EXAMIN TECHNOLOGY CENTER 2800	JER	

portion of metal 31,55 filled the via 27,51.





Continuation of 5. does NOT place the application in condition for allowance because: the metal silicide 13 in Huang provides the interconnect between the layer 12 and the interconnect 44. The metal filled vias are also being used as interconnect metals. The limitation (i.e., the via is "a metal filled opening between various layers of a semiconductor device that provides electrical connection between layers") is not recited in the rejected claim. In fact, Huang teaches a portion of metal 44 filled the via 42a. The silicide layer 9 in Yoo provides the interconnect between the source/drain layer 5b, 7b and the interconnect 31. The metal filled vias are also being used as interconnect metals. The limitation (i.e., the via is "a metal filled opening between various layers of a semiconductor device that provides electrical connection between layers") is not recited in the rejected claim. In fact, Yoo teaches a

Inohara et al. teaches, as shown in Figure 13, the silicide layer 25 is on the semiconductor surface 11 and the stopper film 13a located on the silicide layer 25. Note that the term "on" does not necessarily mean "directly on" and/or "directly contact". The limitation (i.e., the via is "a metal filled opening between various layers of a semiconductor device that provides electrical connection between layers") is not recited in the rejected claim. In fact, Inohara et al. teaches a portion of 16b filled the via 32.